

**Vishay Foil Resistors** 

# Ultra High Precision Foil Wraparound Surface Mount Chip Resistor with TCR of <u>± 0.05 ppm/°C</u> and Power Coefficient of <u>5 ppm</u> at Rated Power and Load Life Stability of <u>± 0.005 %</u> (50 ppm)



## INTRODUCTION

Top View

VSMP Series is the industry's first device to provide high rated power, excellent load life stability along with extremely low TCR all in one resistor.

One of the most important parameters influencing stability is the temperature coefficient of resistance (TCR). Although the TCR of foil resistors is considered extremely low, this characteristic has been further refined over the years. The VSMP Series utilizes ultra high precision Bulk Metal<sup>®</sup> Z-foil. The Z-foil technology provides a significant reduction of the resistive element's sensitivity to ambient temperature variations (TCR) and to self heating when power is applied (power coefficient). Along with the inherently low PCR and TCR, Z-foil technology also provides remarkably improved load life stability, low noise and availability of tight tolerance.

The VSMP has a full wraparound termination which ensures safe handling during the manufacturing process, as well as providing stability during multiple thermal cyclings.

Our application engineering department is available to advise and make recommendations. For non-standard technical requirements and special applications, please contact us using the e-mail address in the footer below.

TABLE 1 - TOLERANCE AND TCR VS. RESISTANCE VALUE (1) (- 55 °C to + 125 °C, + 25 °C Ref.)				
RESISTANCE VALUE (Ω)	TOLERANCE (%)	TYPICAL TCR AND MAX. SPREAD (ppm/°C)		
250 to 125K	± 0.01	± 0.2 ± 1.8		
100 to < 250	± 0.02	± 0.2 ± 1.8		
50 to < 100	± 0.05	± 0.2 ± 2.8		
25 to < 50	± 0.1	$\pm 0.2 \pm 3.8$		
10 to < 25 $\pm 0.25$ $\pm 0.2 \pm 3.8$				

#### Note

(1) For tighter performances and non-standard values up to 150K, please contact Vishay application engineering using the e-mail addresses in the footer below.

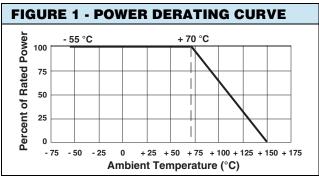
\* Pb containing terminations are not RoHS compliant, exemptions may apply

#### FEATURES

- Temperature coefficient of resistance (TCR): 0.05 ppm/°C typical (0 °C to + 60 °C) 0.2 ppm/°C typical (- 55 °C to + 125 °C, + 25 °C ref.)
- Tolerance: to ± 0.01 %
- Power coefficient "∆R due to self heating":
   5 ppm at rated power
- Power rating: to 750 mW at + 70 °C
- Load life stability: to ± 0.005 % at 70 °C, 2000 h at rated power
- Resistance range: 10  $\Omega$  to 125 k $\Omega$  (for higher and lower values, please contact us)
- Vishay Foil resistors are not restricted to standard values, we can supply specific "as required" values at no extra cost or delivery (e.g. 1K2345 vs. 1K)
- Fast thermal stabilization < 1 s
- Electrostatic discharge (ESD) up to 25 000 V
- Short time overload:  $\leq 0.005$  %
- Non inductive, non capacitive design
- Rise time: 1 ns effectively no ringing
- Current noise: 42 dB
- Voltage coefficient < 0.1 ppm/V
- Non inductive: < 0.08  $\mu$ H
- Non hot spot design
- Terminal finishes available: lead (Pb)-free, tin/lead alloy
- Compliant to RoHS directive 2002/95/EC
- · Matched sets are available on request
- Prototype quantities available in just 5 working days or sooner. For more information, please contact foil@vishaypg.com
- · For better performances please contact us

#### APPLICATIONS

- Automatic test equipment (ATE)
- · High precision instrumentation
- Laboratory, industrial and medical
- Audio
- EB applications (electron beam scanning and recording equipment, electron microscopes)
- Down hole instrumentation
- Communication





**RoHS** 

COMPLIANT

Vishay Foil Resistors



	(Conceptual	Illustration)			TEMPE	RATURE CU	RVE
erloop Capacitance duction in Series Mutual Inductan Reduction due to Change in Current Directio		Current Patt After Trimmin Remov from Sh Changi and		+ 400 - + 300 - + 200 - Mark + 100 - (ppm)- 100 - - 200 - - 300 - - 400 - - 500 -	- 0.16 ppm/° - 0.16 ppm/° - 55 - 25 0 Ambient Tempera Differ	0.14 ppm	om/°C 75 + 100 + 129 ord Slopes for
	Note: Foil shown i	n black, etched spaces	s in white	<ul> <li>The TCR v</li> </ul>	alues for < 100 s and result in dev	$\Omega$ are influenced viation from this cu	by the termina arve.
ABLE 2 -		IS AND LAN		The TCR v composition     in Inches (Mil	n and result in dev limeters)	viation from this cu	by the termina rve.
ABLE 2 -		•		The TCR v composition     in Inches (Mil	n and result in dev	viation from this cu	by the termina
CHIP	DIMENSION	S AND LAN		The TCR v composition     in Inches (Mil	n and result in dev limeters)	viation from this cu Pattern	by the termina irve.
СНІР		IS AND LAN Top View L		• The TCR v composition I in Inches (Mil Recon	n and result in dev limeters) nmended Land F	viation from this cu Pattern X Footprint	иѓvе. Х <sup>(1)</sup>
CHIP SIZE	DIMENSION T	S AND LAN Top View L D U ± 0.005 (0.13) 0.050 (1.27)	D PATTERN W THICKNESS MAXIMUM 0.025 (0.64)	The TCR v composition     In Inches (Mil	n and result in dev limeters) nmended Land F Z (1) 0.122 (3.10)	Viation from this cu Pattern X Footprint G <sup>(1)</sup>	x (1) 0.050 (1.27
CHIP SIZE 0805	DIMENSION T ± 0.005 (0.13) 0.080 (2.03) 0.126 (3.20)	IS AND LAN Top View L L ↓ D ↓ U U U U U U U U U U U U U	D PATTERN W W THICKNESS MAXIMUM 0.025 (0.64) 0.025 (0.64)	<ul> <li>The TCR v composition</li> <li>in Inches (Mil Recon</li> <li>G</li> <li>C</li> <li>C</li> <li>C</li> <li>C</li> <li>C</li> <li>D</li> <li>± 0.005 (0.13)</li> <li>0.015 (0.38)</li> <li>0.020 (0.51)</li> </ul>	n and result in dev limeters) nmended Land F Z (1) 0.122 (3.10) 0.175 (4.45)	viation from this cu Pattern X Footprint G (1) 0.028 (0.71) 0.059 (1.50)	x (1) 0.050 (1.27 0.071 (1.80
CHIP SIZE 0805 1206	DIMENSION T L ± 0.005 (0.13) 0.080 (2.03)	S AND LAN Top View L D U ± 0.005 (0.13) 0.050 (1.27)	D PATTERN W THICKNESS MAXIMUM 0.025 (0.64)	The TCR v composition     In Inches (Mil	n and result in dev limeters) nmended Land F Z (1) 0.122 (3.10)	Viation from this cu Pattern X Footprint G (1) 0.028 (0.71)	x (1) 0.050 (1.27

TABLE 3 - SPECIFICATIONS					
CHIP SIZE	RATED POWER (mW) at + 70 °C	MAX. WORKING VOLTAGE (≤ √P × R)	RESISTANCE RANGE (Ω)	MAXIMUM WEIGHT (mg)	
0805	200	40 V	10 to 8K	6	
1206	300	87 V	10 to 25K	11	
1506	300	95 V	10 to 30K	12	
2010	500	187 V	10 to 70K	27	
2512	750	220 V	10 to 125K	40	

<b>TABLE 4 - LOAD LIFE STABILITY</b> (+ 70 °C for 2000 h)			
CHIP SIZE	MAXIMUM AR LIMITS		
0805	± 0.005 % at 100 mW ± 0.01 % at 200 mW		
1206, 1506	± 0.005 % at 150 mW ± 0.01 % at 300 mW		
2010	± 0.005 % at 200 mW ± 0.01 % at 500 mW		
2512	± 0.005 % at 500 mW ± 0.01 % at 750 mW		

### TABLE 5 - PERFORMANCES

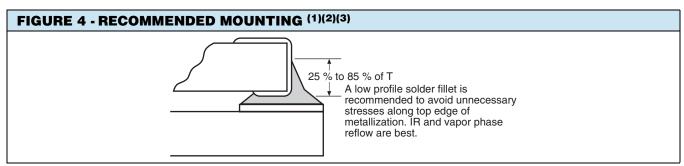
TEST OR CONDITIONS	MIL-PRF-55342 CHARACTERISTIC E ∆R LIMITS	TYPICAL AR LIMITS	MAXIMUM AR LIMITS <sup>(1)</sup>			
Thermal Shock, 100 x (- 65 °C to + 150 °C)	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)			
Low Temperature Operation, - 65 °C, 45 min at Pnom	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)			
Short Time Overload, 6.25 x Rated Power, 5 s	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)			
High Temperature Exposure, + 150 °C, 100 h	± 0.1 %	± 0.01 % (100 ppm)	± 0.02 % (200 ppm)			
Resistance to Soldering Heat	± 0.2 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)			
Moisture Resistance	± 0.2 %	± 0.005 % (50 ppm)	± 0.02 % (200 ppm)			
Load Life Stability + 70 °C for 2000 h at Rated Power	± 0.5 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)			

Note

<sup>(1)</sup> As shown + 0.01  $\Omega$  to allow for measurement errors at low values.



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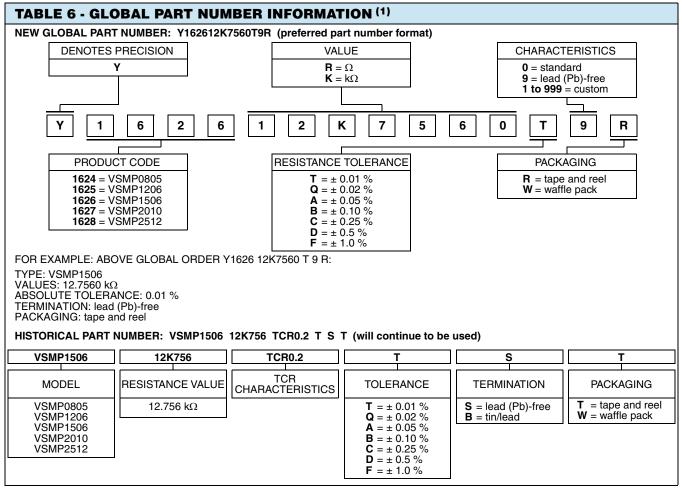


Notes

<sup>(1)</sup> Avoid the use of cleaning agents which could attack epoxy resins, which form part of the resistor construction

<sup>(2)</sup> Vacuum pick up is recommended for handling

(3) Soldering iron may damage the resistor



Note

<sup>(1)</sup> For non-standard requests, please contact application engineering.



Vishay Precision Group

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