

# N-Channel 240-V (D-S) MOSFET

PRODUCT SUMMARY							
Part Number	V <sub>DS</sub> Min (V)	$r_{DS(on)}$ ( $\Omega$ )	V <sub>GS(th)</sub> (V)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)		
TN2404K	240	4 @ V <sub>GS</sub> = 10 V	0.8 to 2.0	0.2	4.87		
TN2404KL/BS107KL		4 @ V <sub>GS</sub> = 10 V	0.8 to 2.0	0.3	4.67		

#### **FEATURES**

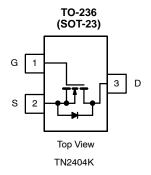
- Low On-Resistance: 4 Ω
- Secondary Breakdown Free: 260 V
- Low Power/Voltage Driven
- Low Input and Output Leakage
- Excellent Thermal Stability

#### **BENEFITS**

- Low Offset Voltage
- Full-Voltage Operation
- Easily Driven Without Buffer
- Low Error Voltage
- No High-Temperature "Run-Away"

#### **APPLICATIONS**

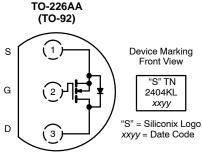
- High-Voltage Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Transistors, etc.
- Telephone Mute Switches, Ringer Circuits
- Power Supply, Converters
- Motor Control



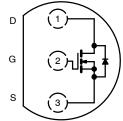
Marking Code: K1ywl

K1 = Part Number Code for TN2404K

y = Year Code w = Week Code l = Lot Traceability



(TO-18 Lead Form)



Top View

BS107KL

TO-92-18RM

Device Marking Front View

> "S" BS 107KL *xxyy*

"S" = Siliconix Logo xxyy = Date Code

Top View TN2404KL

ORDERING INFORMATION				
Standard Part Number	Lead (Pb)-Free Part Number	Option		
TN2404K-T1	TN2404K-T1—E3	With Tape and Reel Folding Option		
TN2404KL-TR1	TN2404KL-TR1—E3	Spool Option		
BS107KL-TR1	BS107KL-TR1—E3	Spool Option		

Parameter Drain-Source Voltage		Symbol	TN2404K	TN2404KL/BS107KL		
		V <sub>DS</sub>	240			
Gate-Source Voltage		$V_{GS}$	±20			
O II	T <sub>A</sub> = 25°C		0.2	0.3		
Continuous Drain Current (T <sub>J</sub> = 150°C)	T <sub>A</sub> = 70°C	ן ו <sub>ס</sub> י	0.16	0.25	Α	
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	0.8	1.4	1	
D 0: : ::	T <sub>A</sub> = 25°C		0.36	0.8	14/	
Power Dissipation	T <sub>A</sub> = 70°C	P <sub>D</sub>	0.23	0.51	W	
Thermal Resistance, Junction-to-Ambient		R <sub>thJA</sub>	350 <sup>b</sup>	156	°C/W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>sta</sub>	-55 to 150		°C	

#### Notes

a. Pulse width limited by maximum junction temperature.

b. Surface mounted on an FR4 board.

# TN2404K/TN2404KL/BS107KL

# Vishay Siliconix



				Limits		
Parameter	Symbol	Test Conditions	Min	Min Typ <sup>a</sup>	Max	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 100 \mu\text{A}$	240	257		V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	0.8	1.65	2.0	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm 20$ V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 192 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
		T <sub>J</sub> = 55°C			10	
On-State Drain Current <sup>b</sup>		$V_{DS} = 10 \ V, V_{GS} = 10 \ V$	0.8			A
	I <sub>D(on)</sub>	$V_{DS} = 10 \ V, V_{GS} = 4.5 \ V$	0.5			
Drain-Source On-Resistance <sup>b</sup>		$V_{GS} = 10 \text{ V}, I_D = 0.3 \text{ A}$		2.2	4	Ω
	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 0.2 \text{ A}$		2.3	4	
		$V_{GS} = 2.5 \text{ V}, I_D = 0.1 \text{ A}$		2.4	6	
Forward Transconductanceb	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 0.3 \text{ A}$		1.6		S
Diode Forward Voltage	V <sub>SD</sub>	$I_S = 0.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic <sup>a</sup>			•	•		•
Total Gate Charge	$Q_g$			4.87	8	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 192 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		0.56		
Gate-Drain Charge	Q <sub>gd</sub>			1.53		
Turn-On Time	t <sub>d(on)</sub>			5	10	nS
	t <sub>r</sub>	$V_{DD} = 60 \text{ V}, R_L = 200 \Omega$		12	20	
A 0# Tim-	t <sub>d(off)</sub>	$I_D \simeq 0.3  \text{A},  V_{\text{GEN}} = 10  \text{V},  R_{\text{G}} = 25  \Omega$		35	60	
turn-Off Time	t <sub>r</sub>			16	25	

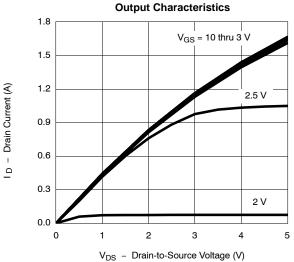
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

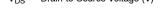
 $<sup>\</sup>begin{array}{ll} \text{Notes} \\ \text{a.} & \text{For DESIGN AID ONLY, not subject to production testing.} \\ \text{b.} & \text{Pulse test: } \text{PW} \leq 300~\mu\text{s} ~\text{duty cycle} \leq 2\%. \\ \end{array}$ 

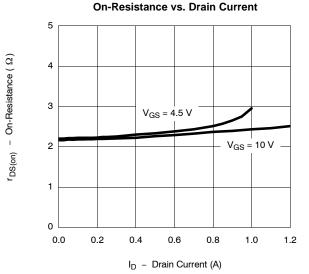


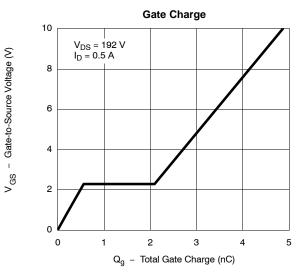


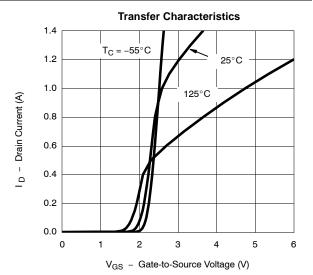
### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

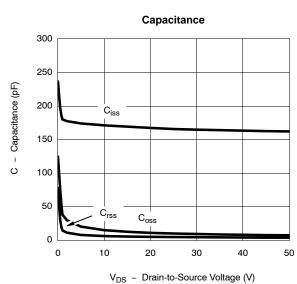


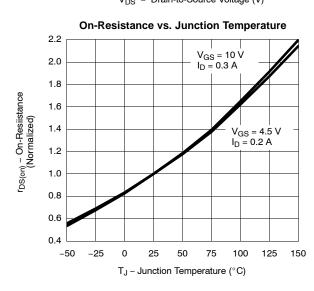






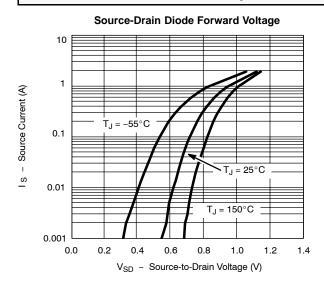


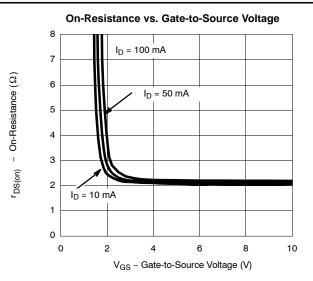


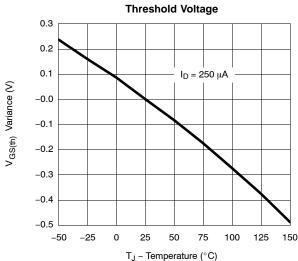




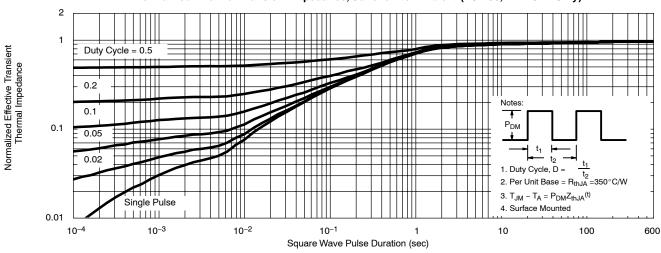
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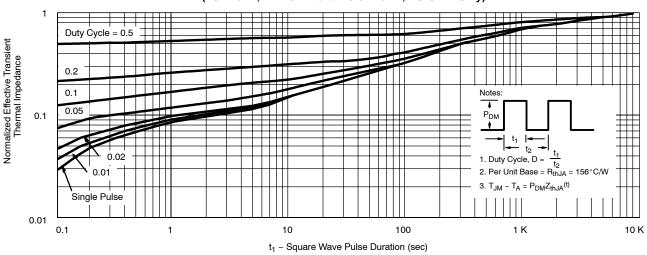


### Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-236, TN2404K Only)



### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

# Normalized Effective Transient Thermal Impedance, Junction-to-Ambient (TO-226AA, TN2404KL and TO-92-18RM, BS107KL Only)



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