

Vishay High Power Products

Schottky Rectifier, 1.0 A



| PRODUCT SUMMARY | | | |
|--------------------|----------------|--|--|
| I _{F(AV)} | 1.0 A | | |
| V _R | 90/100 V | | |
| I _{RM} | 1 mA at 125 °C | | |

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The MBRS190TRPbF, MBRS1100TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | |
|-----------------------------------|----------------------------------|-------------|-------|--|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | | |
| I _{F(AV)} | Rectangular waveform | 1.0 | А | | |
| V _{RRM} | | 90/100 | V | | |
| I _{FSM} | t _p = 5 μs sine | 870 | А | | |
| V _F | 1.0 Apk, T _J = 125 °C | 0.63 | V | | |
| TJ | Range | - 55 to 175 | °C | | |

| VOLTAGE RATINGS | | | | |
|--------------------------------------|------------------|--------------|---------------|-------|
| PARAMETER | SYMBOL | MBRS190TRPbF | MBRS1100TRPbF | UNITS |
| Maximum DC reverse voltage | V _R | 90 | 100 | V |
| Maximum working peak reverse voltage | V _{RWM} | | | v |

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|--|--------------------|---|--|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum average forward current | I _{F(AV)} | 50 % duty cycle at T_L = 147 °C, rectangular waveform | | 1.0 | |
| Maximum peak one cycle non-repetitive surge current | 1 | 5 μs sine or 3 μs rect. pulse | Following any rated load condition and with rated V _{RRM} applied | 870 | А |
| | IFSM | 10 ms sine or 6 ms rect. pulse | | 50 | |
| Non-repetitive avalanche energy | E _{AS} | $T_J = 25 \text{ °C}, I_{AS} = 0.5 \text{ A}, L = 8 \text{ mH}$ | | 1.0 | mJ |
| Repetitive avalanche current | I _{AR} | | | А | |

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| ELECTRICAL SPECIFICATIONS | | | | | |
|---------------------------------|--------------------------------|---|---------------------------------------|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum forward voltage drop | V _{FM} ⁽¹⁾ | 1 A | T _J = 25 °C | 0.78 | V |
| See fig. 1 | | VFM (1) TA | T _J = 125 °C | 0.62 | v |
| Maximum reverse leakage current | I _{BM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | 0.5 | mA |
| See fig. 2 | IRM \'' | T _J = 125 °C | | 1.0 | |
| Typical junction capacitance | CT | V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C | | 42 | pF |
| Typical series inductance | Ls | Measured lead to lead 5 mm from package body | | 2.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated V _R 10 000 V | | V/µs | |

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | |
|---|--|--------------------------------------|-------------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | - 55 to 175 | °C |
| Maximum thermal resistance, junction to lead | R _{thJL} ⁽²⁾ | DC operation See fig. 4 | 36 | °C/W |
| Maximum thermal resistance, junction to ambient | R _{thJA} | DC operation | 80 | 0/11 |
| Approximate weight | | | 0.10 | g |
| | | | 0.003 | 0Z. |
| Marking device | | Case style SMB (similar to DO-214AA) | V19/ | V10 |

Notes

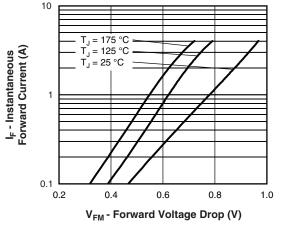
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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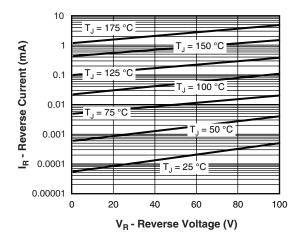
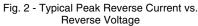


Fig. 1 - Maximum Forward Voltage Drop Characteristics Fig. 2 - Typi



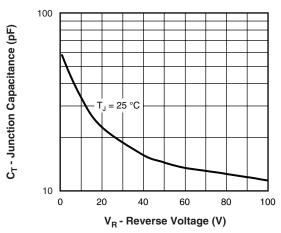


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

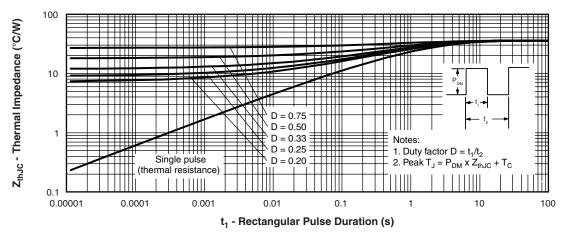
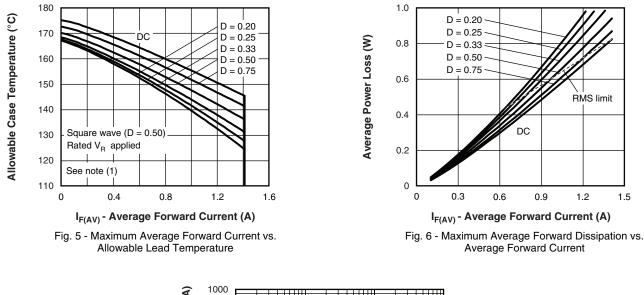
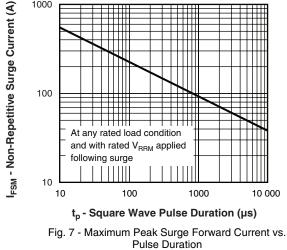


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$

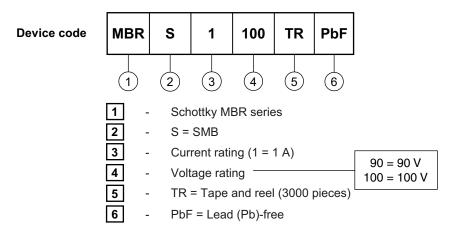
 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$





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ORDERING INFORMATION TABLE



| LINKS TO RELATED DOCUMENTS | | | | |
|-------------------------------------|--------------------------|--|--|--|
| Dimensions www.vishay.com/doc?95017 | | | | |
| Part marking information | www.vishay.com/doc?95029 | | | |
| Packaging information | www.vishay.com/doc?95034 | | | |



Vishay

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