

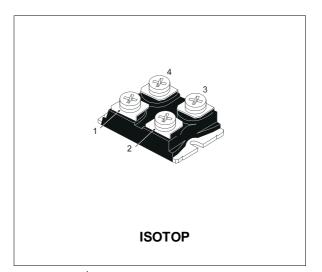
ESM2012DV

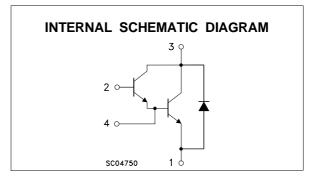
NPN DARLINGTON POWER MODULE

- HIGH CURRENT POWER BIPOLAR MODULE
- VERY LOW Rth JUNCTION TO CASE
- SPECIFIED ACCIDENTAL OVERLOAD AREAS
- ULTRAFAST FREEWHEELING DIODE
- FULLY INSULATED PACKAGE (UL COMPLIANT)
- EASY TO MOUNT
- LOW INTERNAL PARASITIC INDUCTANCE

INDUSTRIAL APPLICATIONS:

- MOTOR CONTROL
- UPS
- DC/DC & DC/AC CONVERTERS





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VCEV	Collector-Emitter Voltage ($V_{BE} = -5 V$)	150	V
V _{CEO(sus)}	Collector-Emitter Voltage $(I_B = 0)$	120	V
V _{EBO}	Emitter-Base Voltage $(I_{C} = 0)$	7	V
Ic	Collector Current	120	А
Ісм	Collector Peak Current (t _p = 10 ms)	180	A
IB	Base Current	2	А
Івм	Base Peak Current (t _p = 10 ms)	4	A
P _{tot}	Total Dissipation at $T_c = 25 \ ^{\circ}C$	175	W
V _{isol}	Insulation Withstand Voltage (RMS) from All Four Terminals to Exernal Heatsink	2500	V
T _{stg}	Storage Temperature	-55 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-case (transistor)	Max	0.7	°C/W
R _{thj-case}	Thermal Resistance Junction-case (diode)	Max	0.9	°C/W
R _{thc-h}	Thermal Resistance Case-heatsink With Conductive			
	Grease Applied	Max	0.05	°C/W

ELECTRICAL CHARACTERISTICS ($T_{case} = 25 \ ^{\circ}C$ unless otherwise specified)

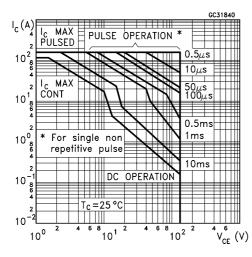
Symbol Parameter		Test Conditions	Min.	Тур.	Max.	Unit
I _{CER} #	Collector Cut-off Current ($R_{BE} = 5 \Omega$)				1.5 10	mA mA
I _{CEV} #	Collector Cut-off Current (V _{BE} = -5V)				1 7	mA mA
I _{EBO} #	Emitter Cut-off Current $(I_C = 0)$	V _{EB} = 5 V			1	mA
Vceo(sus)*	Collector-Emitter Sustaining Voltage (I _B = 0)	$I_C = 5 A$ $L = 15 mH$ $V_{clamp} = 125 V$	125			V
h _{FE} *	DC Current Gain	$I_{C} = 100 \text{ A}$ $V_{CE} = 5 \text{ V}$		1200		
V _{CE(sat)} *	Collector-Emitter Saturation Voltage			1.25 1.35 1.5 1.65	1.5 2	V V V V
$V_{BE(sat)^*}$	Base-Emitter Saturation Voltage			2.3 2.35	3	V V
di _C /dt	Rate of Rise of On-state Collector		200	230		A/µs
V _{CE} (3 µs)⊷	Collector-Emitter Dynamic Voltage			2	3	V
V _{CE} (5 µs)••	Collector-Emitter Dynamic Voltage			1.8	2.5	V
t _s t _f t _c	Storage Time Fall Time Cross-over Time			0.9 0.15 0.3	2 0.3 0.6	μs μs μs
V _{CEW}	Maximum Collector Emitter Voltage Without Snubber		125			V
VF*	Diode Forward Voltage	$I_F = 100 \text{ A}$ $T_j = 100 ^{\circ}\text{C}$		0.92	1	V
I _{RM}	Reverse Recovery Current			10	14	A

* Pulsed: Pulse duration = $300 \,\mu$ s, duty cycle 1.5 % # See test circuits in databook introduction To evaluate the conduction losses of the diode use the following equations:

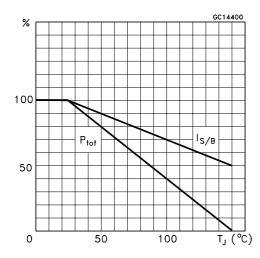
 $V_F = 0.66 + 0.0034 \ I_F \qquad P = 0.66 \ I_{F(AV)} + 0.0034 \ I^2_{F(RMS)}$

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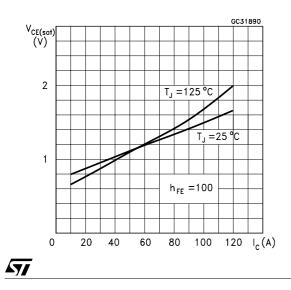
Safe Operating Areas



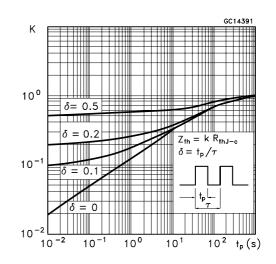
Derating Curve



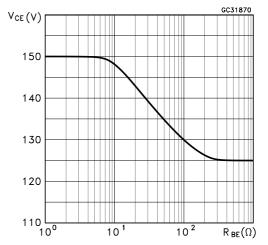
Collector Emitter Saturation Voltage



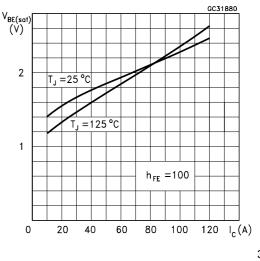
Thermal Impedance



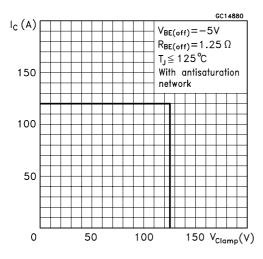
Collector-emitter Voltage Versus base-emitter Resistance



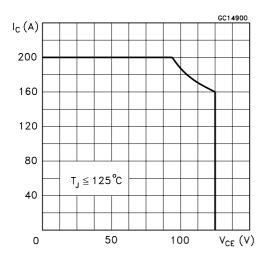




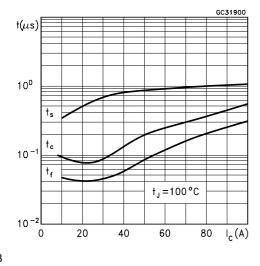
Reverse Biased SOA



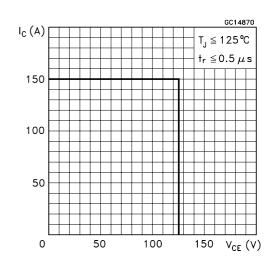
Reverse Biased AOA



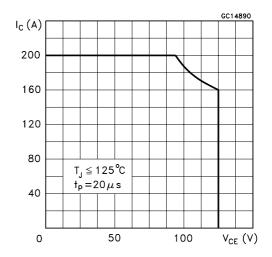
Switching Times Inductive Load

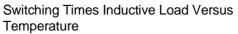


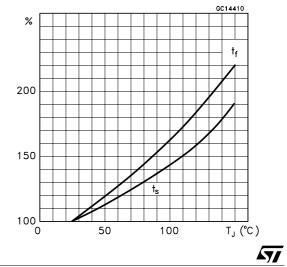
Foward Biased SOA



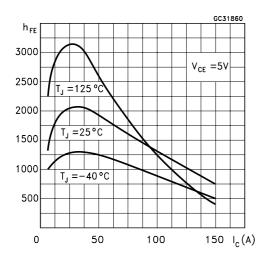




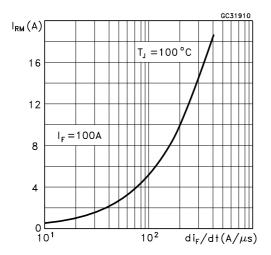




Dc Current Gain



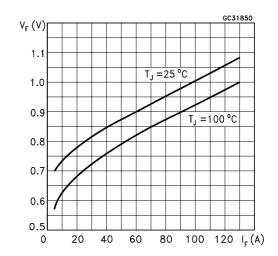
Peak Reverse Current Versus diF/dt

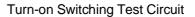


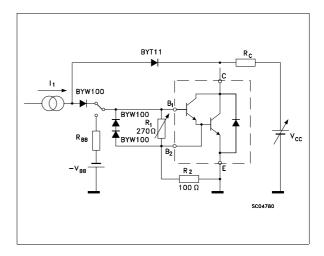


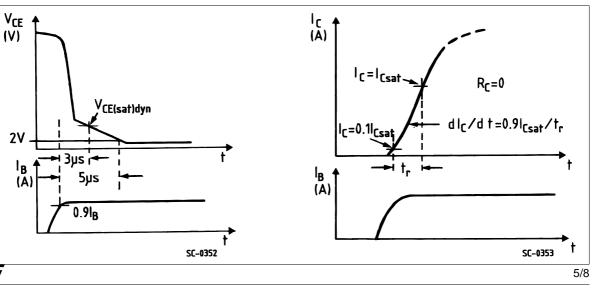
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Typical V_F Versus I_F

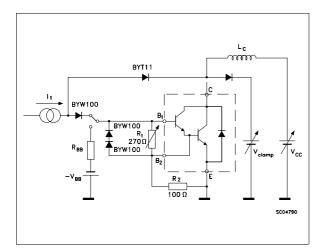




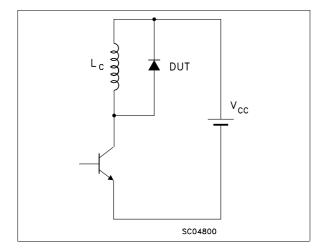




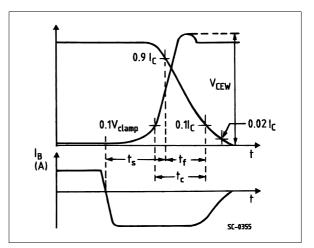
Turn-on Switching Test Circuit



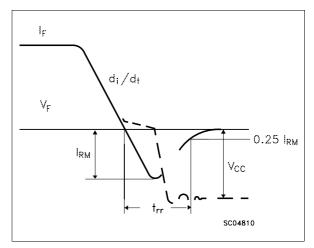
Turn-off Switching Test Circuit of Diode



Turn-off Switching Waveforms



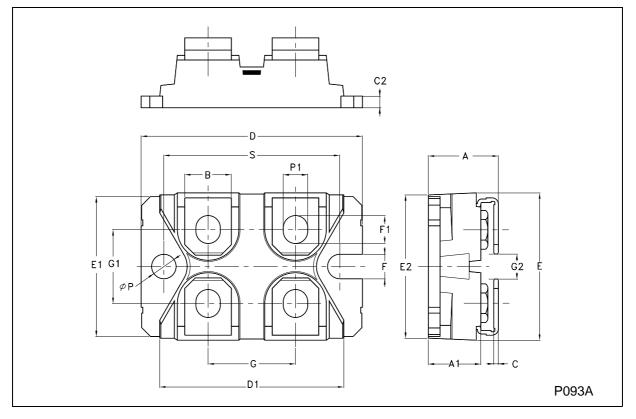
Turn-off Switching Waveform of Diode



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DIM.		mm			inch	
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	11.8		12.2	0.465		0.480
A1	8.9		9.1	0.350		0.358
В	7.8		8.2	0.307		0.322
С	0.75		0.85	0.029		0.033
C2	1.95		2.05	0.076		0.080
D	37.8		38.2	1.488		1.503
D1	31.5		31.7	1.240		1.248
E	25.15		25.5	0.990		1.003
E1	23.85		24.15	0.938		0.950
E2		24.8			0.976	
G	14.9		15.1	0.586		0.594
G1	12.6		12.8	0.496		0.503
G2	3.5		4.3	0.137		1.169
F	4.1		4.3	0.161		0.169
F1	4.6		5	0.181		0.196
Р	4		4.3	0.157		0.169
P1	4		4.4	0.157		0.173





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