Vishay Sfernice



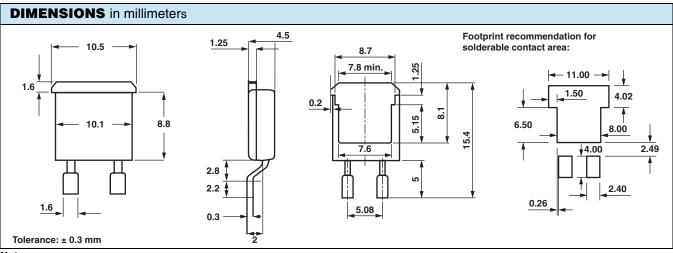
COMPLIANT

Surface Mounted Power Resistor Thick Film Technology



FEATURES

- AEC-Q200 qualified
- 20 W at 25 °C case temperature
- Surface mounted resistor TO-263 (D2PAK) style package
- Wide resistance range from 0.01 Ω to 550 $k\Omega$
- Non inductive
- · Resistor isolated from metal tab
- Solder reflow secure at 270 °C/10 s
- Compliant to RoHS Directive 2002/95/EC



- For the asssembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C
- Power dissipation is 2.8 W at an ambient temperature of 25 °C when mounted on a double sided copper board using FR4 standard, 70 µm of copper, 39 mm x 30 mm x 1.6 mm

ELECTRICAL SPECIFICATIONS			
Resistance Range	0.01 Ω to 550 k Ω		
Tolerances (Standard)	± 1 % to 10 %		
Power Rating and Thermal Resistance	20 W at 25 °C (case temperature) R _{TH (j - c)} : 6.5 °C/W		
Temperature Coefficient	See Special Features table Standard: ± 150 ppm/°C		
Limiting Element Voltage U _L	250 V		
Dielectric Strength IEC 60115-1	2000 V _{rms} - 1 min - 10 mA max. (between terminals and board)		
Insulation Resistance	$\geq 10^6 \text{M}\Omega$		
Inductance	≤ 0.1 µH		
Critical Resistance	3.12 kΩ		

SPECIAL FEATURES				
Resistance Values	≥ 0.010	≥ 0.045	≥ 0.1	≥ 0.5
Tolerances	± 1 % at ± 10 %			
Requirement Temperature Coefficient (TCR) (- 55 °C + 150 °C) IEC 60115-1	± 1100 ppm/°C		± 150 ppm/°C	

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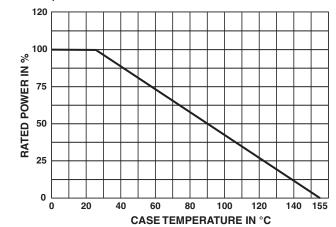
MECHANICAL SPECIFICATIONS			
Mechanical Protection Molded			
Resistive Element	Thick film		
Substrate	Alumina		
Connections	Tinned copper		
Weight	2.2 g max.		

ENVIRONMENTAL SPECIFICATIONS			
Temperature Range - 55 °C to 155 °C			
	IEC 60695-11-5		
Flammability	2 applications 30 s separated by 60 s		

DIMENSIONS	
Standard Package	TO-263 style (D ² PAK)

POWER RATING

The temperature of the case should be maintained within the limits specified.



PERFORMANCE			
TESTS	CONDITIONS	REQUIREMENTS	
Momentary Overload	IEC 60115-1 §4.13 $2 P_{\rm r} 5 {\rm s}$ for $R < 2 \Omega$ 1.6 $P_{\rm r} 5 {\rm s}$ for $R \ge 2 \Omega$ $U_{\rm S} < 1.5 U_{\rm L}$	± (0.25 % + 0.005 Ω)	
Rapid Temperature Change	IEC 60115-1 Test Na 5 cycles - 1 h - 55 °C to + 155 °C	± (0.5 % + 0.005 Ω)	
Load Life	IEC 60115-1 1000 h at + 25 °C	± (1 % + 0.005 Ω)	
Humidity (Steady State)	IEC 60115-1 IEC 60068-2-3 Test Ca: 56 days RH 95 %	± (0.5 % + 0.005 Ω)	
Vibration	MIL STD-202 Method 204 - Test D 10 Hz to 2000 Hz	± (0.2 % + 0.005 Ω)	
Terminal Strength	IEC 60115-1		
Shock	IEC 60115-1 IEC 60068-2-27 Saw-tooth: 100 gn/6 ms	± (0.5 % + 0.005 Ω)	

ASSEMBLY SPECIFICATIONS			
For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C			
TESTS	CONDITIONS	REQUIREMENTS	
Resistance to Soldering Heat	IEC 60115-1 IEC 60068-2-58 Solder Bath method: 270 °C/10 s	± (0.5 % + 0.005 Ω)	
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020C 85 °C/85 % RH/168 h	Level: 1 + Pass requirements of TCR Overload and Dielectic Strength after MSL	

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CHOICE OF THE BOARD

The user must choose the board according to the working conditions of the component (power, room temperature). Maximum working temperature must not exeed 155 °C. The dissipated power is simply calculated by the following ratio:

$$P \, = \, \frac{\Delta T}{[\,R_{TH \, (j \, - \, c)}\,] + [\,R_{TH \, (c \, - \, a)}\,]}^{\, (1)}$$

P: Expressed in W

ΔΤ: Difference between maximum working temperature and room temperature

Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal R_{TH (i - c)}:

resistance of the component: 6.5 °C/W.

Thermal resistance value measured between outer side of the resistor and room temperature. It is the thermal $R_{TH(c-a)}$:

resistance of the solder layer (according the quality of the soldering) and the thermal resistance of the board.

Example:

 $R_{TH (c-a)}$ for D2TO20 power rating 2.5 W at ambient temperature + 25 °C.

Thermal resistance R_{TH (i - c)}: 6.5 °C/W

Considering equation (1) we have:

$$\Delta T = 155 \,^{\circ}\text{C} - 25 \,^{\circ}\text{C} = 130 \,^{\circ}\text{C}$$

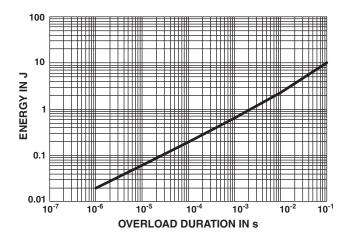
$$R_{TH (j-c)} + R_{TH (c-a)} = \Delta T/P = 130/2.5 = 52 \text{ °C/W}$$

$$R_{TH (c-a)} = 52 \text{ °C/W} - 6.5 \text{ °C/W} = 45.5 \text{ °C/W}$$

ACCIDENTAL OVERLOAD

In any case the applied voltage must be lower than the maximum overload voltage of 375 V. The values indicated on the graph below are applicable to resistors in air or mounted onto a board.

ENERGY CURVE



Single Pulse:

These informations are for a single pulse on a cold resistor at 25 °C (not already used for a dissipation) and for pulses of 100 ms maximum duration.

The formula used to calculate E is:

$$E = P \times t = \frac{U^2}{R} \times t$$

with:

E(J): Pulse energy P(W): Pulse power t(s): Pulse duration U(V): Pulse voltage $R(\Omega)$: Resistor

The energy calculated must be less than that allowed by the graph.

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Repetitive or Superimposed Pulses:

The following formula is used to calculate the "equivalent" energy of a repetitive pulse or the "equivalent energy" of a pulse on a resistor that is already dissipating power.

$$E_{c} = E \times \left(1 + \frac{P_{a}}{P}\right)$$

with:

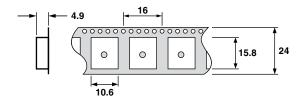
 $E_{\rm c}$ (J): Equivalent pulse energy E(J): Known pulse energy Resistor power rating

 P_a : Mean power being dissipated

The energy calculated must be less than that allowed by the graph and the average power dissipated (P_a) must not exceed the continuous power of resistor.

PACKAGING

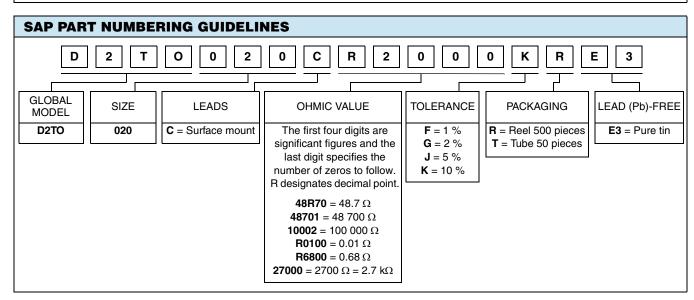
- Reel
- Tube
- Tape dimensions (mm) for reel:



MARKING

Model, style, resistance value (in Ω), tolerance (in %), manufacturing date, Vishay Sfernice trademark

ORDERING INFORMATION						
D2TO	020	С	100 k Ω	± 1 %	XXX	e3
MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	TOLERANCE	CUSTOM DESIGN	LEAD (Pb)-FREE
				$F = \pm 1 \%$ $G = \pm 2 \%$ $J = \pm 5 \%$ $K = \pm 10 \%$	Optional on request: Shape, etc.	





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